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	USN	Į		15EE51
			Fifth Semester B.E. Degree Examination, Dec.2018/Jan.20	19
			Management and Entrepreneurship	
	Tir	ne:	3 hrs. Max. M	Marks: 80
			Note: Answer FIVE full questions, choosing ONE full question from each mod	lule.
			Module-1	
ice.	1	a.	Briefly explain the functional area's of management.	(06 Marks)
ract		b.	Bring out the difference between management and administration.	(05 Marks)
nalp		c.	Discuss the importance of planning.	(05 Marks)
as n			OR	
ated	2	a.	Explain the various roles a manager plays.	(05 Marks)
s.		b.	Explain the different types of plans.	(06 Marks)
age 11 be		C.	Briefly explain the steps in decision making.	(05 Marks)
nk p. (, wi	2	0	What are the various principles of ergeniaction?	
5 bla = 50	3	a. h	What is committees and explain the different types of committees?	(05 Marks)
ning 2+8		с.	Explain with a diagram Maslow's theory of motivation	(05 Marks)
smai g, 4,			OR	(00 Marks)
ne re	4	a.	Distinguish between centralization and decentralization	(05 Marks)
on th vritt		b.	Define staffing. Explain importance and functions of staffing.	(06 Marks)
nes o		c.	Explain the meaning and importance of coordination.	(05 Marks)
ss li atic			Module-3	
l cro	5	a.	Explain social responsibilities of business towards different groups.	(05 Marks)
onal d /o		b.	Explain the different stages of an Entrepreneural process.	(06 Marks)
diag r an		c.	Explain the role of an entrepreneurs in economic development in INDIA.	(05 Marks)
aw uato			OR	
ly dı eval	6	a.	What are the qualities of an Entrepreneur?	(05 Marks)
sori I to		b.	What are the functions of an entrepreneurs?	(05 Marks)
luqu		c.	Write a note on social audit and business ethics.	(06 Marks)
con n, ap			Module-4	
/crs,	7	a.	Explain the different policies for development of SSI in INDIA.	(06 Marks)
answ		b.	Explain the impact of Iliberalization, Privatization and Globalization on SSI's.	(05 Marks)
our a iden		C.	write a note on TECSOK (Technical Consultancy Services Organization of Kari	nataka).
s of			OR	(05 Marks)
letir aling	8	a.	What are the different roles of SSI's?	(05 Marks)
duio		b.	Explain the Impact of GATT and WTO.	(05 Marks)
)n ci uy t		c.	Write a note on (i) KIADB (ii) KSFC	(06 Marks)
1. C 2. A			Module-5	
: :	9	a.	Explain the factors to be considered for selection of projects.	(05 Marks)
t Nc		b.	List the various contents of the project report.	(06 Marks)
rtan		С.	Explain PERT and CBM.	(05 Marks)
oduu			OR	
1	10	a.	Explain the process of project appraisal.	(06 Marks)
		b.	Explain the meed and significance of project report.	(05 Marks)
		c.	What are the guidelines by planning commission for a project report? Explain.	(05 Marks)
			* * * *	

	CBCS SCHEME					
USN		15EE52				
	Fifth Semester B.E. Degree Examination, Dec.2018/Jan.2019 Microcontroller					
Tim	: 3 hrs. Max. Ma	rks: 80				
	Note: Answer any FIVE full questions, choosing ONE full question from each module.					
1	Module-1         Draw the programming model of 8051 μc. Explain the function of following :         (i) Accumulator, Register B and CPU Registers.         (ii) Program controller, Stack and Stack pointer         After adding the following data, show the states of CY, AC and P flags:	(10 Marks)				
	(i) 55h and AAM (ii) 12h and 62h	(06 Marks)				
2	<b>OR</b> Explain the internal RAM organization of 8051 with suitable diagrams. ( How many address lines are required for accessing the data in the following mer while data is organized as bytes:	( <b>08 Marks</b> ) nory ICs,				
,	(i) 512 bytes RAM (ii) &K RAM. Explain the program ROM space allocation for the following : (i) $EA = 0$ for 8751 chip (ii) $EA = V_{CC}$ with both on-chip and off-chip ROM for	04 Marks) 8751. 04 Marks)				
3	Module-2Explain the following assembler directives:(i) DB(ii) ORG(iii) EQUExplain the working of the instruction SUBB when borrow = 0 and borrow = 1.(A student has to take 6 courses in a semester. The marks of the student out of 25 ain RAM locations 50h onwards. Write a program to find the average marks and aRegister R6.	06 Marks) 06 Marks) are stored save it in 04 Marks)				
4 a	OR Write a program to complement the value AAh, 800 times. With respect to Port 0, explain the fallowing : (i) Working of Port 0 (ii) Dual role of Port 0	04 Marks)				
(	(iii) Example program to use Port 0 as input and output. (iii) Write a program to generate a square wave of 50% duty cycle on bit 5 of Port-2. (iii)	08 Marks) 04 Marks)				
5 a	<u>Module-3</u> Write an 8051 'C' program to send values – 4 to +4 to Port P1. (4) Write 8051 'C' program to toggle all the bits of P0 and P2 continuously with 250 m (4) Write an 8051 'C' program to convert packed BCD 0×28 to ASCII and display	05 Marks) s delay. 05 Marks) bytes on				
	P1 and P2. ((	06 Marks)				

2

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  - n )

#### OR

- a. Explain Mode-1 programming of 8051 timer. Describe the different steps to program in 6 (08 Marks) Mode-1.
  - b. Write 8051 assembly program to generate square wave with  $t_{ON} = 3$ ms and  $t_{OFF} = 10$ ms on all pins of Port 0. System clock is 22 MHz. Use timer 0 in Mode-1. (08 Marks)

### Module-4

- a. Describe bit status of SCON register. 7
  - b. Write 8051 assembly program to receive the data in serial form and send it out to Port-0 in parallel form. Save the data in RAM location 62h. Assume baud rate = 9600. Use timer 1 in (05 Marks) Mode 2.
  - c. Calculate the baud rate if f = -2, SMOD = 1, XTIAL = 11.0592 MHz. Is this baud rate supported by IBM PCS? (03 Marks)

### OR

- a. Explain the steps in executing an interrupt. 8
  - b. Write 8051 assembly program in which 8051 reads data from P1 and writes it to P2 continuously while giving a copy of it to serial COM port to be transferred serially. Assume baud rate = 9600 and XTAL = 11.0592 MHz. Use timer -1 in mode 2. (08 Marks) (04 Marks)
  - c. Explain the bit status of IP Register.

#### Module-5

- a. Calculate the address range of  $16 \times 2$  LCD and  $20 \times 1$  LCD. 9 (03 Marks)
  - b. Explain the internal architecture of ADC 0804 and its timing diagram to convert analog data (10 Marks) to digital form.
  - c. Consider 8 bit ADC. Assume  $V_R = 5V$ . Calculate the 8 bit digital output when  $V_{in} = 3V$ . (03 Marks)

#### CR

a. Write 8051 assembly program to rotate a stepper motor 64° in clockwise direction. The 10 motor has step angle of 2°. Use 4 step sequence and draw the schematic diagram. Steps per revolution = 180, number of rotor teeth = 45. Movement per 4 step sequence =  $8^{\circ}$ .

(08 Marks)

b. What is PWM technique? Explain bidirectional motor control using L293 chip. If SW = 0, the dc motor moves clockwise and if SW = 1, the dc motor moves counter-clockwise. Draw the schematic diagram. Write 8051 assembly program to do this. (08 Marks)

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2 of 2

(08 Marks)

(04 Marks)



# Fifth Semester B.E. Degree Examination, Dec.2018/Jan.2019 Power Electronics

Time: 3 hrs.

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2

Max. Marks: 80

Note: Answer any FIVE full questions, choosing ONE full question from each module.

### Module-1

- a. Explain any five types of power electronics converter system and also specify the form of input and output waveforms. (10 Marks)
- b. With block diagram, explain the peripheral effects and remedies of power electronic converter systems. (06 Marks)

### OR

- a. With circuit diagram and waveforms explain uncontrolled single phase full wave rectifier with RL load. (08 Marks)
  - b. With circuit diagram and waveforms explain diode switched RL load with necessary equations. (08 Marks)

### Module-2

- 3 a. Explain the different types of base drive control circuits for BJT.
  - b. In the bipolar transistor circuit shown in Fig.Q.3(b) B various between 5 and 50. The load resistance  $R_C = 10\Omega$ ,  $V_{CC} = 180V$  and  $V_{BB} = 10V$ . If  $V_{CE(sat)} = 1.0V$  and  $V_{BE(sat)} = 1.4V$ . Calculate:
    - i) The value of  $R_B$  that results in saturation with an overdrive factor of 6.
    - ii) The forced  $B_f$
    - iii) The total power loss in the transistor.

(08 Marks)

(08 Marks)



OR

4 a. Sketch the structure of n-channel enhancement type MOSFET and explain its working principle. (08 Marks)

1 of 3

- b. The IGBT shown in the circuit of Fig.Q.4(b) Has the following data:  $t_{on} = 3\mu$ sec,  $t_{off} = 1.2\mu$ sec, duty cycle D = 0.7,  $V_{ce(sat)} = 2V$ ,  $f_s = 1$  HHz. Calculate:
  - i) Average load current.
  - ii) Conduction power loss.
  - iii) Switching power loss during turn-on and turn off.

(08 Marks)



### Module-3

- 5 a. Derive an expression for the anode current of thyristor with the help of transistor analogy.
  - b. For the circuit shown in Fig.Q.5(b) if the latching current is 4mA, calculate the minimum width of gate pulse required to properly turn on the SCR. (06 Marks)



### OR

6 a. With circuit diagrams and wave forms explain the method of protection of SCR. (10 Marks)
b. How many SCRS are required in a series string to withstand a d.c. voltage of 5000V in steady state, if the SCRS have steady state voltage rating of 1200V and the steady state derating factor is 10%. Assuming maximum difference in leakage current of SCRS to be 12mA, calculate the value of voltage sharing resistance to be used. (06 Marks)

### Module-4

7 a. With circuit diagram and waveforms explain dual converters. (08 Marks)
b. With circuit diagram and waveforms explain the operation of three phase full converters.

(08 Marks)

- 8 a. With circuit diagram and wave forms explain the operation of full wave ac voltage controller connected to resistive load. And also obtain the equation for RMS output voltage. (08 Marks)
  - b. A single phase half wave ac voltage controllar has an input voltage of 200V and a load resistance of  $10\Omega$ . The firing angle of each thyristor is 30 degree in each positive half cycle. Calculate:
    - i) Average output voltage
    - ii) RMS output voltage
    - iii) Power output
    - iv) Power input.

9

(08 Marks)

### Module-5

a. With circuit diagram and waveforms explain the working of class D chopper. (08 Marks)
b. A step up chopper has input voltage of 200V and output voltage of 660V. If the nonconducting time of thyristor is 100µsec. Calculate the pulse width of the output voltage. If the pulse width is halved for constant frequency operation, calculate the new output voltage. (08 Marks)

### OR

- 10 a. With circuit diagram explain the operation of a single phase full bridge inverter supplying a resistive load. (08 Marks)
  - b. Explain any two modulation techniques available for voltage control of a single phase inverter. (08 Marks)



(08 Marks)

### Module-3

- State and prove the following properties in continuous time Fourier transform i) Linearity 5 a. (10 Marks) ii) Time shift iii) Time differentiation.
  - Find the Fourier Transformation of  $x(t) = e^{-at} u(t)$ , a > 0. (06 Marks) b.

### OR

Using partial fraction expansion and linearity to determine the inverse Fourier transform of 6 a.  $-i\omega$ (08 Marks)

$$K(j\omega) = \frac{1}{(j\omega)^2 + 3j\omega + 2}$$
(6) Warks)

Find the frequency response and impulse response of the system described by the b. differential equation.

$$\frac{d^2 y(t)}{dt^2} + 3\frac{dy(t)}{dt} + 2y(t) = 2\frac{dx(t)}{dt} + x(t)$$

b.

Х

## Module-4

State and prove the following properties in Discrete time Fourier transform 7 a. (10 Marks) i) Frequency shift ii) Parseval's theorem. Find DTFT of the following signal

i) 
$$x[n] = \left(\frac{1}{2}\right)^{n+2} u[n]$$
 ii)  $x[n] = 2(3)^n u[-n]$  (06 Marks)

### OR

Using DTFT, find the total solution to the difference equation for discrete time  $n \ge 0$ . 8 a.  $5y(n+2) - 6y(n+1) + y(n) = (0.8)^n u(n)$ (08 Marks)

Determine the difference equation description for the system with the following impulse b.

(08 Marks) response  $h[n] = \delta[n] + 2$ u(n) +

### Module-

9	a.	What is region of convergence? List any 5 pr	operties of ROC. (07 Mark	KS)
	b.	Find the z-transform and ROC of the signal	$x[n] = -b^n u[-n-1]$ (05 Mark	ks)
	c.	State and prove time shift property.	(04 Mark	ks)

### OR

Determine the inverse z-transform of x(z)10 a.

$$(z) = \frac{1}{1 - 1.5z^{-1} + 0.5z^{-2}} \text{ for ROC} |z| > 1; \frac{1}{2} < |z| < 1.$$
(06 Marks)

Consider a causal discrete time sequence whose output y(n) and x(n) are related by b.

$$y(n) - \frac{5}{6}y(n-1) + \frac{1}{6}y(n-2) = x(n)$$

ii) Find its impulse response h[n]. (10 Marks) i) Find its system function

\* \* \* \* \*

2 of 2



Determine the size of the copper conductor for a 2-core cable required to carry a maximum с. current of 60A. Length of the cable used is 100 meters and declared supply voltage is 240V ac. [current ratings of cables shown in Table.Q3(c) may be referred]. (06 Marks)

Size of cable		Current rating in Amps		Approximate Ampere –meter	
No. and die of wire	Area in mm <sup>2</sup>	2 core cable	3 or 4 core cable	per volt drop	
19/1.12	19.35	62	50	1050	
19/1.32	25.80	74	59	1475	
19/1.626	38.70	97	78	2200	

OR

- With reference to internal electrification of building, explain how to determine the a. following:
  - i) total load ii) rating of main switch and distribution board iii) number of circuits. (06 Marks)
  - b. Explain the three essential points which must be considered while determining the size of the conductor for internal wiring for a given circuit. (06 Marks)
  - c. A residential building is to be provided with electrical installation to be connected to a single phase, 240V, 50Hz AC supply. Details of the electrical points to be installed in the building are as follows :

Lighting circuit : light points – 8 nos. 5A socket outlets – 7nos.

Heating circuit : 15A socket outlets – 2 nos. (1 for kit, 1 for both room).

Determine : i) total number of sub-circuits ii) rating of distribution board iii) size of cable for lighting circuit iv) size of cable for heating circuit. (04 Marks)

4

1 of 2

(05 Marks)

### 15EE553

(08 Marks)

### Module-3

- 5 a. What do you understand by 'Service Line'? What are the types of service connections that are in use? Compare the different types of service connections. (05 Marks)
  - b. Prepare an estimation of materials for providing OH service connection to a single storied building with 240V, 1φ, 50Hz AC supply. The building has a light and Fan load of 5 KW. The supply is to be given from an overhead line 20m away from the building. [Assume missing data].
  - c. State the important considerations regarding motor installation wiring. (05 Marks)

### OR

- 6 a. With simple sketches, explain any two methods of installation of overhead service lines based on the prevailing conditions of the building. (04 Marks)
  - b. Explain how to determine the following for the purpose of wiring : i) input power to a motor ii) input current to motors iii) size of the cable iv) rating of the fuse. (08 Marks)
  - c. A 10 HP(metric), 415V, 3 phase, 50 Hz induction motor is to be installed at the middle of a 30m × 10m workshop. The motor has been supplied along with STAR/DELTA starter. The meter – board shall be located at one corner of the workshop. Draw :
    - i) The plan of the workshop showing positions of the equipments
    - ii) The layout of the wiring. The wiring is to be of surface conduit type. (04 Marks)

### Module-4

- 7 a. Explain the functions of the following in relevance to OH transmission and distribution.
   i) phase plates ii) beads of jumpers. (04 Marks)
  - Explain the necessity of 'Earthing of Transmission Line Supports' and also show with a neat sketch how earthing of a 'Line Support' is done using pipe earthing. (08 Marks)
  - c. A 1 km long overhead distribution line of 415V, 3 phase, 50 Hz is to be erected along a straight route from a 100KVA, 11/0.433 0.240 KV pole mounted sub-station. The line is to be laid with 6/1 × 3.00mm ACSR conductor on RCC poles of 9m length. The span between adjacent poles is to be maintained at 50m. Draw a rough sketch of the route and find : i) the number of poles required ii) the total length of the ACSR conductor required iii) the length of the 8 SWG GI wire for earthing. (04 Marks)

### OR

- 8 a. Explain what is meant by 'repairing and jointing of overhead ACSR transmission conductors'. How repairing or jointing is done? (04 Marks)
  - b. List out the various points to be considered at the time of erection of overhead lines. (08 Marks)
  - c. An overhead distribution line of 415V, 3 phase, 50Hz is to be erected along a straight route. The length of the line is 300 meters and the end supports are terminal structures. The span between adjacent Poles is 50 meters. Consider 4 SWG bare copper wires for phase, neutral and street light control: 8 SWG galvanized steel wire for earth wire. Find the :

i) number of intermediate poles and the number of terminal structures ii) length of wire of each size for the line. (04 Marks)

### Module-5

9 a. Explain the requirement of the following in a substation :
 i) substation auxiliaries supply
 ii) substation earthing.

b. Draw the single line diagram for a 10MVA, 33/11KV substation and prepare an estimation of materials required, with their complete specification. (08 Marks)

### OR

- 10 a. Explain the functions of the following in a substation :
  - i) lighting arresters ii) isolators iii) earthing switch iv) batteries. (08 Marks)
     b. Draw the single line diagram for 132/33KV substation with main and transfer bus having 2 × 40 MVA transformers. Prepare on estimation of materials required, with their complete specification. (08 Marks)



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### 15EE563

- Briefly explain four problems faced in exploiting tidal energy. (04 Marks) 8 a. b. Describe the construction and working of fixed dome type biogas plant and its material (06 Marks) aspects. (06 Marks)
  - With diagram, explain updraft gasifier and write its applications area. C.

9

# Module-5

- (04 Marks) Write advantages of sea wave power. a. Explain how the ocean temperature differences can be used to generate electrical power b. (06 Marks)
  - using open cycle system. Describe with diagram, principle of oscillating water column ocean wave machine. C.

(06 Marks)

### OR

10	а	Briefly explain types of devices for harnessing wave energy.	(04 Marks)
10	а.	Diferily explain open of a start and the file and	(0( Manlie)
	h	Describe the 'Closed – Cycle' OTEC system, with the help of diagram.	(UO Marks)
	υ.	Desenibe die Closed Office office,	(OC Manles)
	с.	State the merits and demerits of OTEC plants.	(06 Marks)